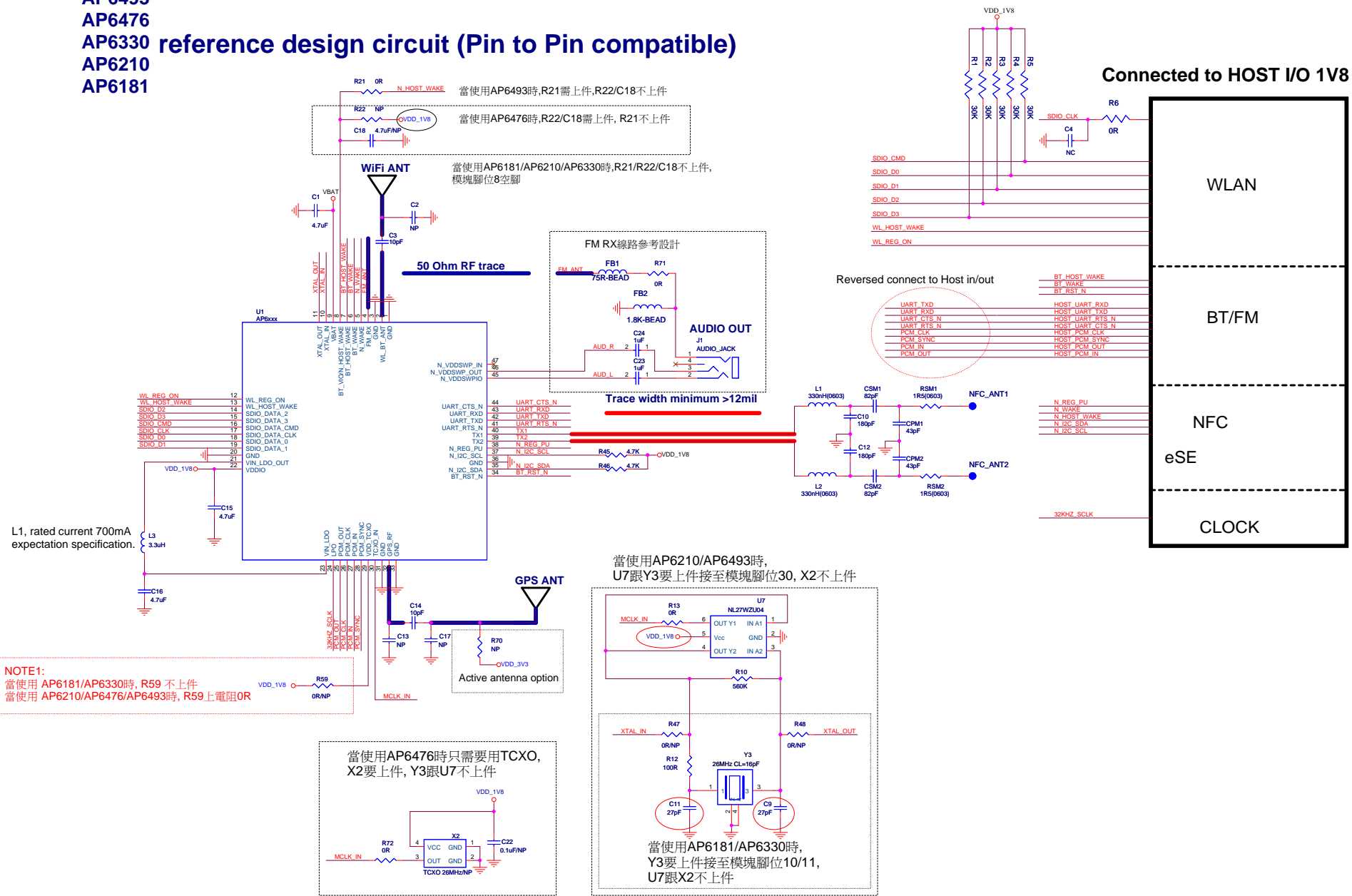


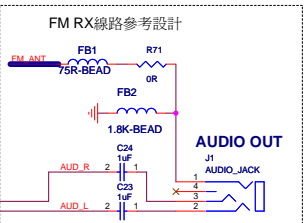
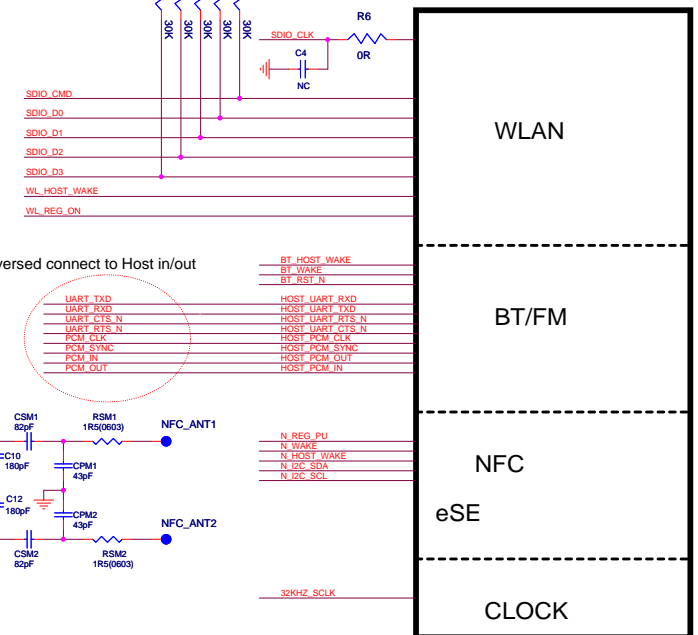
# History

Date	Rev	Content
11/20/2012	V1.0	Initial release
12/05/2012	V1.1	Add NFC pin definition
12/07/2012	V1.2	Modify to divider circuit from level shift at BT reset/wale/host wake
12/10/2012	V1.3	Modify 1V8 I/O design for 1.8V Host system
01/03/2013	V1.4	Add NOTE1 design consideration, Active GPS antenna option
01/10/2013	V1.5	Add FM Circuit Option

**AP6493  
AP6476  
AP6330 reference design circuit (Pin to Pin compatible)  
AP6210  
AP6181**

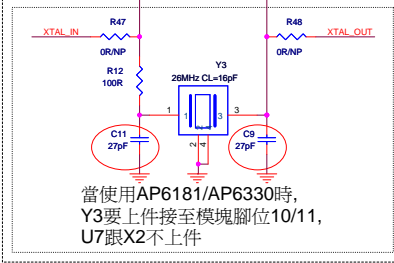
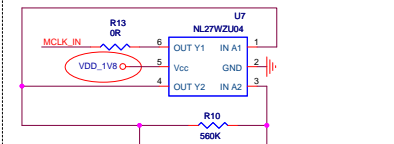


Connected to HOST I/O 1V8

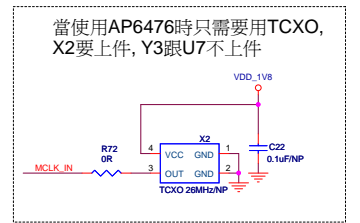


Trace width minimum >12mil

當使用AP6210/AP6493時，U7跟Y3要上件接至模塊腳位30，X2不上件



當使用AP6181/AP6330時，Y3要上件接至模塊腳位10/11，U7跟X2不上件



當使用AP6476時只需要用TCXO，X2要上件，Y3跟U7不上件

L1, rated current 700mA expectation specification.

NOTE1:  
當使用 AP6181/AP6330時，R59 不上件  
當使用 AP6210/AP6476/AP6493時，R59上電阻0R

AMPAK Technology co.,Ltd			
Title:	AP6XXX PIN to PIN compatible design for 1V8 I/O		
Size:	c	File:	Sheet: 1 of 1
Directory:	-design name-		Rev: 1.5
Design:	Joe	Appvd:	Brian
		Now date:	Present date: