Allwinner A31/A31s NandFlash Support List

V1.0

2014-03-27

- Revision History

Revision No.	History	Date	Author
V1.0	1. Initial version build.	2014.03.27	Tony.Chow

- Note Introduction

Note	Description
SNT	Support but no sample test
SST	Support and sample test
√	Support and mass produce

- Attention

This support list is based on the latest nand flash driver version.

- Support List

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	NOTE
SANSUMG	K9K2G08U0(A/M)	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	K9W4G08U0M	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	K9W4G08U1M	SLC	2	2	2K+64	128K+4K	512M		TSOP	SST
	K9K4G08U0(A/M)	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	K9F4G08U0(A/M)	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	K9W8G08U1M	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	K9K8G08U0(A/M)	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	K9K8G08U1M	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	K9WAG08U0(A/M)	SLC	1	1	2K+64	128K+4K	2G		TSOP	SST
	K9WAG08U1(A/M)	SLC	2	2	2K+64	128K+4K	2G		TSOP	SST
	K9F8G08U0(A/M)	SLC	1	1	4K+128	256K+8K	1G		TSOP	SST
	K9KAG08U0M	SLC	1	1	4K+128	256K+8K	2G		TSOP	SST
	K9F1G08U0(A/B/M)	SLC	1	1	2K+64	128K+4K	128M		TSOP	SST
	K9F2G08U0(A/M)	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	K9G4G08U0(A/M)	MLC	1	1	2K+64	256K+8K	512M		TSOP	SST
	K9L8G08U0M	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	K9G8G08U0M	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	K9LAG08U0M	MLC	1	1	2K+64	256K+8K	2G		TSOP	SST
	K9GAG08U0M/D	MLC	1	1	4K+128	512K+16K	2G		TSOP	SST
	K9LBG08U0M/D	MLC	1	1	4K+128	512K+16K	4G		TSOP	SST
	K9HBG08U1M	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
SANSUMG	K9HCG08U0M	MLC	1	1	4K+128	512K+16K	8G		TSOP	SST
	K9HCG08U1M	MLC	2	2	4k+128	512K+16K	8G		TSOP	SST
	K9GBG08X0M	MLC	1	1	8K+436	1M+54.5K	4G	32nm	TSOP	SST
	K9GAG08U0E	MLC	1	1	8K+436	1M+54.5K	2G	32nm	TSOP	SST
	K9G8G08U0C	MLC	1	1	8K+436	1M+54.5K	1G		TSOP	SST
	K9GBG08U0A	MLC	1	1	8K+640	1M+60K	4G	27nm	TSOP	√
	K9GBGD8U0A	MLC	1	1	8K+640	1M+60K	4G	27nm	LGA	√
	K9GBG08U0B	MLC	1	1	8K+1K	1M+128K	4G	21nm	TSOP	√
	K9LCG08U0A	MLC	1	1	8K+640	1M+80K	8G	27nm	TSOP	√
	K9HDG08U1A	MLC	2	2	8K+640	1M+80K	16G	27nm	TSOP	√
	K9LCG08U0B	MLC	1	1	8K+1K	1M+128K	8G	21nm	TSOP	√
HYNIX	HY27UF081G2(A/M)	SLC	1	1	2K+64	128K+4K	128M		TSOP	SST
	HY27UF082G2(A/M)	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	HY27UH084G2M	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	HY27UF084G2M	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	HY27UG084G2M	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	HY27UF084G2B	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	HY27UG088G5M	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	HY27UG088G5B	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	HY27H088G2M	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	HY27UG088G2M	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	HY27UH08AG5M	SLC	2	2	2K+64	128K+4K	2G		TSOP	SST
	HY27U4G8T2BTR	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
HYNIX	H27U1G8F2B	SLC	1	1	2K+64	128K+4K	128M		TSOP	SST
	HY27UF082G2B	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	HY27UT084G2M	MLC	1	1	2K+64	256K+8K	512M		TSOP	SST
	HY27UU088G5M	MLC	2	2	2K+64	256K+8K	1G		TSOP	SST
	HY27UT088G2M	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	HY27UT088G2M	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	HY27UT088G2M	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	HY27UV08AG5M	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	HY27UU08AG5M	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	HY27UU08AG5M	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	HY27UU08AG5M	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	H27UAG8T2A	MLC	1	1	4K+128	512K+16K	2G		TSOP	SST
	HY27UV08BG5M	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	HY27UV08BG5M	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	HY27UV08BG5M	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	H27UBG8U5A	MLC	2	2	4K+224	512K+28K	4G		TSOP	SST
	H27UCG8V5M	MLC	2	2	4K+128	512K+16K	8G		TSOP	SST
	H27UCG8V5A	MLC	2	2	4K+224	512K+28K	8G	32nm	TSOP	SST
	H27UBG8T2A	MLC	1	1	8K+448	2M+112K	4G	32nm	TSOP	SST
	H27UCG8U5(D)A	MLC	2	2	8K+448	2M+112K	8G	32nm	TSOP	SST
	H27UDG8V5A	MLC	2	2	8K+448	2M+112K	16G	32nm	TSOP	SST
	H27UBG8T2M	MLC	1	1	4K+224	512K+28K	4G		TSOP	SST
	H27UAG8T2B	MLC	1	1	8K+448	2M+112K	2G	32nm	TSOP	SST
	H27U8G8T2B	MLC	1	1	4K+128	512K+16K	1G		TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
HYNIX	H27UBG8T2BTR	MLC	1	1	8K+448	2M+112K	4G	26nm	TSOP	SST
	H27UCG8U5BTR	MLC	2	2	8K+448	2M+112K	8G	26nm	TSOP	SST
	H27UCG8T2MYR	MLC	1	1	8K+448	2M+112K	8G	26nm	LGA	√
	H27UBG8T2CTR	MLC	1	1	8K+640	2M+160K	4G	20nm	TSOP	√
	H27UCG8T2ATR	MLC	1	1	8K+640	2M+160K	8G	20nm	TSOP	√
	H27UCG8T2BYR	MLC	1	1	16K+1280	4M+320K	8G	20nm	LGA	√
	H27UCG8T2BTR	MLC	1	1	16K+1280	4M+320K	8G	20nm	TSOP	√
	H27UCG8T2ETR	MLC	1	1	16K+1664	4M+416K	8G	16nm	TSOP	SST
MICRON	MT29F2G08AAC	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	MT29F4G08BAB	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	MT29F4G08AAA	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	MT29F8G08FAB	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	MT29F8G08DAA	SLC	2	2	2K+64	128K+4K	1G		TSOP	SST
	MT29F8G08BAB	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	MT29F16G08FAB	SLC	2	2	2K+64	128K+4K	2G		TSOP	SST
	MT29F4G08MAA	MLC	1	1	2K+64	256K+8K	512M		TSOP	SST
	MT29F8G08QAA	MLC	2	2	2K+64	256K+8K	1G		TSOP	SST
	MT29F8G08MAA	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	MT29F16GTAA	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	MT29F16G08QAA	MLC	2	2	2K+64	256K+8K	2G		TSOP	SST
	MT29F32G08TAA	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	MT29F32G08TAA	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	MT29F32G08CBAAA	MLC	1	1	4K+218	512K+27K	4G	34nm	TSOP	SST
	MT29F32G08CBABA	MLC	1	1	4K+224	1024K+56K	4G	34nm	TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
MICRON	MT29F64G08TAA	MLC	2	2	4K+128	512K+16K	8G	34nm	TSOP	SST
	MT29F64G08CFAAA	MLC	2	2	4K+218	512K+27K	8G	34nm	TSOP	SST
	MT29F128G08CJAAA	MLC	2	2	4K+218	512k+27K	16G	34nm	TSOP	SST
	MT29F128G08CJABA	MLC	2	2	4K+218	512k+27K	16G	34nm	TSOP	SST
	MT29F64G08CBAAA	MLC	1	1	8K+448	2M+112K	8G	25nm	TSOP	√
	MT29F32G08CBACA	MLC	1	1	4K+224	1M+56K	4G	25nm	TSOP	√
	MT29F16G08CBABA	MLC	1	1	4K+224	1M+56K	2G	34nm	TSOP	SST
	MT29F16G08CBACA	MLC	1	1	4K+224	1M+56K	2G	25nm	TSOP	SST
	MT29F128G08CFAAA	MLC	2	2	8K+448	2M+112K	16G	25nm	TSOP	SST
	MT29F64G08CBABA	MLC	1	1	8K+744	2M+186K	8G	20nm	TSOP	√
	MT29F32G08CBADA	MLC	1	1	8K+744	2M+186K	4G	20nm	TSOP	√
INTEL	JS29F02G08AAN	SLC	1	1	2K+64	128K+4K	256M		TSOP	SST
	JS29F04G08BAN	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	JS29F04G08AAN	SLC	1	1	2K+64	128K+4K	512M		TSOP	SST
	JS29F08G08FAN	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	JS29F08G08BAN	SLC	1	1	2K+64	128K+4K	1G		TSOP	SST
	JS29F16G08FAN	SLC	2	2	2K+64	128K+4K	2G		TSOP	SST
	JS29F08G08AAM	MLC	1	1	2K+64	256K+8K	1G		TSOP	SST
	JS29F16G08CAM	MLC	2	1	2K+64	256K+8K	2G		TSOP	SST
	JS29F32G08FAM	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	JS29F32G08FAM	MLC	2	2	2K+64	256K+8K	4G		TSOP	SST
	JS29F32G08AAMD2	MLC	1	1	4K+218	512K+27K	4G		TSOP	SST
	JS29F32G08CAMC1	MLC	2	2	4K+218	512K+27K	4G		TSOP	SST
	JS29F64G08CAMD2	MLC	2	2	4K+218	512K+27K	8G		TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
INTEL	JS29F32G08AAMDB	MLC	1	1	4K+224	1M+56K	4G	34nm	TSOP	SST
	JS29F64G08FAMC1	MLC	2	2	4K+218	512K+27K	8G	34nm	TSOP	SST
	JS29F64G08CAMDB	MLC	2	2	4K+224	1M+56K	8G	34nm	TSOP	SST
	JS29F64G08AAME1	MLC	1	1	8K+448	2M+112K	8G	25nm	TSOP	SST
	JS29F64G08ACME3	MLC	1	1	8K+448	2M+112K	8G	25nm	TSOP	SST
	JS29F16B08CCME3	MLC	2	2	8K+448	2M+112K	16G	25nm	TSOP	SST
	JS29F32B08JCME3	MLC	4	4	8K+448	2M+112K	32G	25nm	TSOP	SST
	JS29F64G08ACMF3	MLC	1	1	8K+744	2M+186K	8G	20nm	TSOP	SST
	JS29F16B08CCMF3	MLC	2	2	8K+744	2M+186K	16G	20nm	TSOP	SST
	JS29F32B08JCMF3	MLC	4	4	8K+744	2M+186K	32G	20nm	TSOP	SST
SANDISK	SDTNQGAMA-008G	MLC	1	1	16k+1280	4M+320K	8G	19nm	TSOP	√
	SDTNQFAMA-004G	MLC	1	1	16k+1280	4M+320K	4G	19nm	TSOP	√
	SDTNRGAMA-008G	MLC	1	1	16k+1280	4M+320K	8G	A19nm	TSOP	SST
	SDTNQGAMA-008GP	MLC	1	1	16k+1280	4M+320K	8G	A19nm	TSOP	SST
TOSHIBA	TC58NVG0S3ETA00	SLC	1	1	2K+64	128K+4K	128M		TSOP	SST
	TH58NVG6D1DTH20	MLC	2	2	4K+224	512K+27K	8G		TSOP	SST
	TH58NVG5D1DTG20	MLC	2	2	4K+218	512K+27K	4G		TSOP	SST
	TC58NVG4D2FTA00	MLC	1	1	8K+448	1M+56K	2G		TSOP	SST
	TC58NVG5D2FTA00	MLC	1	1	8K+448	1M+56K	4G		TSOP	SST
	TC58NVG6D2FTA20	MLC	2	2	8K+448	1M+56K	8G		TSOP	SST
	TC58NVG4D2HTA00	MLC	1	1	8K+640	1M+80K	2G	24nm	TSOP	SST
	TC58NVG5D2HTA00	MLC	1	1	8K+640	1M+80K	4G	24nm	TSOP	SST
	TC58NVG6D2GTA00	MLC	1	1	8K+640	1M+80K	8G	24nm	TSOP	SST
	TH58NVG7D2GTA20	MLC	2	2	8K+640	1M+80K	16G	24nm	TSOP	SST

VENDER	PART NUMBER	CELL	CEs	RBs	PAGE SIZE	BLOCK SIZE	CAPACITY	PROCESS	PACKAGE	Note
TOSHIBA	TC58TEG5DCJTA00	MLC	1	1	16K+1280	4M+320K	4G	19nm	TSOP	SST
	TC58NVG6DCJTA00	MLC	1	1	16K+1280	4M+320K	8G	19nm	TSOP	√
	TC58TEG6DCJTA00	MLC	1	1	16K+1280	4M+320K	8G	19nm	TSOP	√
	TC58TEG6DDJTA00	MLC	1	1	16K+1280	4M+320K	8G	19nm	TSOP	SST
	TH58TEG7DCJTA20	MLC	2	2	16K+1280	4M+320K	16G	19nm	TSOP	SST
	TH58TEG7DDKTA20	MLC	2	2	16K+1280	4M+320K	16G	19nm	TSOP	SST
	TC58TEG6DDKTA00	MLC	1	1	16K+1280	4M+320K	8G	19nm	TSOP	SST

• Declaration

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